

ISO/IEC JTC 1/SC 22/WG 5 "Fortran"
Convenorship: ANSI
Convenor: Lionel Steve Mr



2021 WG5 Meeting Attendance List

| Document type | Related content | Document date | Expected action |
|------------------------------|--|---------------|---------------------------|
| Meeting / Attendance list | Meeting: VIRTUAL 21 Jun 2021 | 2021-07-02 | INFO by 2021-06-21 |

Attendees of Virtual Meeting of ISO/IEC JTC1/SC22/WG5
June 21, 23, 28 and 30, 2021 (UTC)

Steve Lionel (self, USA) WG5 convenor

Brian Friesen (Lawrence Berkeley National Laboratory, USA) PL22.3 convenor

Bill Ash (SC22, USA)

Reinhold Bader (Leibniz Supercomputing Centre, Germany and DIN)

Richard Bleikamp (AMD, USA)

Reuben Budiardja (Oak Ridge, USA)

Ondřej Čertík (LANL, USA)

Daniel Chen (IBM, Canada)

Tom Clune (NASA, USA)

Malcolm Cohen (NAG, UK)

Bob Corbett (self, USA)

Milan Curcic (Dancing Bear, USA)

Siddhartha Ghosh (NCAR, USA)

Jeff Hammond (NVIDIA, USA)

Magne Haveraaen (University of Bergen, Norway)

Hidetoshi Iwashita (HPFPC, Japan)

Zach Jibben (LANL, USA)

Gary Klimowicz (Dancing Bear, USA)

Mark LeAir (NVIDIA, USA)

Bill Long (HPE, USA)

Raghu Maddhipatia (AMD, USA)

Divya Mangudi (Intel, USA)

Lorri Menard (Intel, USA)

David Muxworthy (self, UK)

Vipul Parekh (Resenex, USA)

John Reid (JKR Associates, UK)

Brad Richardson (Sourcery, USA)

Damian Rouson (Sourcery, USA)

Anton Shterenlikht (HPE, UK)

Nathan Sircombe (Arm, UK)

Jon Steidel (Intel, USA)

Masayuki Takata (Edogawa University, Japan)

Srinath Vadlamani (Arm, USA)